

Characteristic Impedance of a Wide Slotline on Low-Permittivity Substrates (Short Papers)

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Computed results on the characteristic impedance of wide slots etched on an electrically thin substrate of low dielectric constant ϵ_r are presented. These results combined with those in [1] provide design data for these slotlines. Curves are presented for $\epsilon_r = 2.22, 3.0, 3.8, \text{ and } 9.8$. Comparison is shown for the characteristic impedance between the present calculations and those available in the literature for high- ϵ_r substrates. Empirical formulas, based on least-square curve fitting, are presented for the normalized slot wavelength λ/λ_0 and the characteristic impedance Z_0 over the range $0.0015 \leq W/\lambda_0 \leq 1.0$, $0.006 \leq d/\lambda_0 \leq 0.06$, $2.22 \leq \epsilon_r \leq 9.8$.

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